

218446



PATENT
1248-0497P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Haruhiko SHIGEMASA et al Conf.:
Appl. No.: 09/552,701 Group: unassigned
Filed: April 19, 2000 Examiner: UNASSIGNED
For: MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF AND IC CARD PACKING
MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF

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L E T T E R

Assistant Commissioner for Patents
Washington, DC 20231

March 13, 2001

Sir:

Further to the Information Disclosure Statement and accompanying Form PTO-1449 filed on February 7, 2001, it is respectfully requested that the Examiner initial and enter the attached corrected PTO-1449 form for the above-identified application.

The dates of foreign patent documents submitted in the previous 1449 form were reversed and, therefore, have been corrected with the attached PTO-1449 form.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or

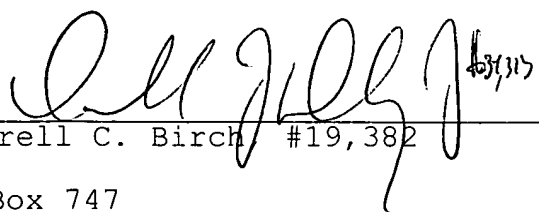
Appl. No. 09/552,701

credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Respectfully submitted,

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